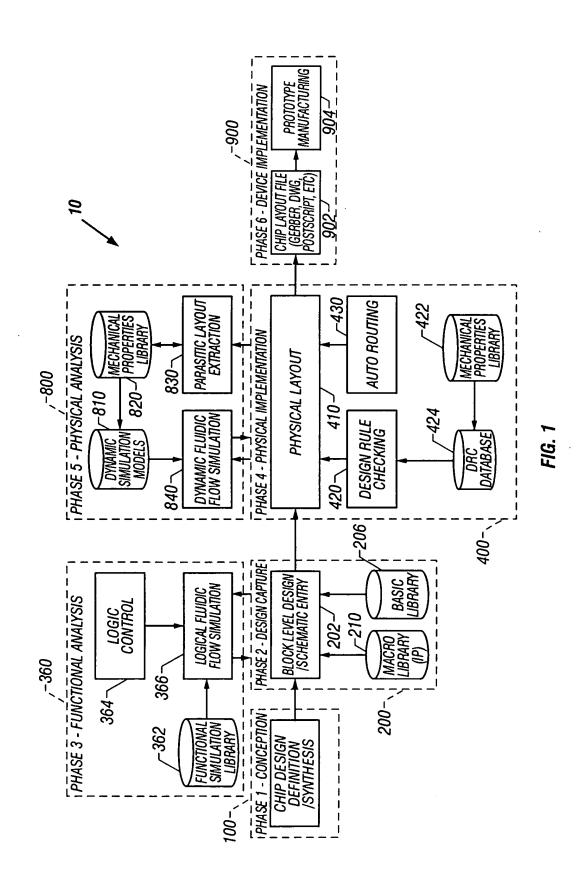
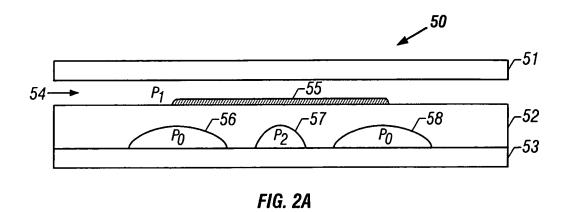
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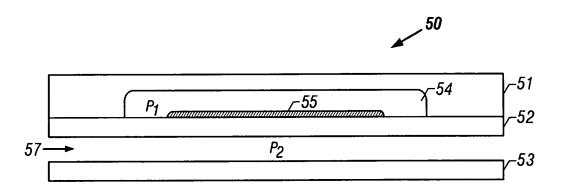


FIG. 2B

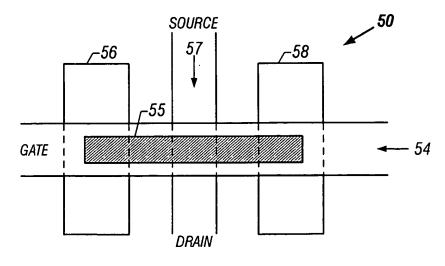
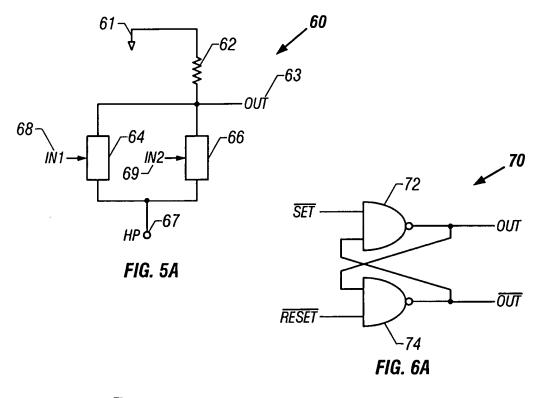


FIG. 3

FIG. 4A FIG. 4G FIG. 4B FIG. 4H FIG. 4C HP 0-FIG. 41 FIG. 4D FIG. 4E FIG. 4J FIG. 4K FIG. 4F

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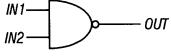
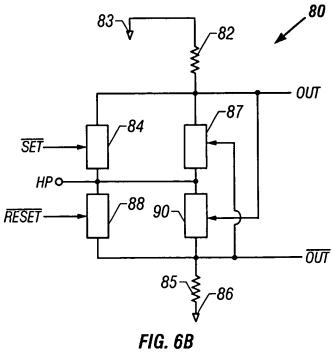


FIG. 5B



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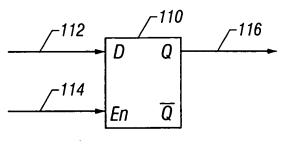


FIG. 7A

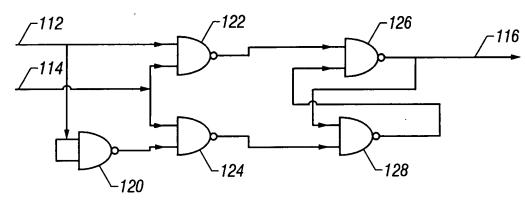
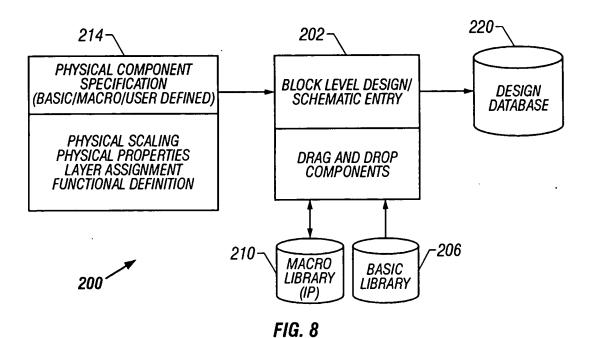
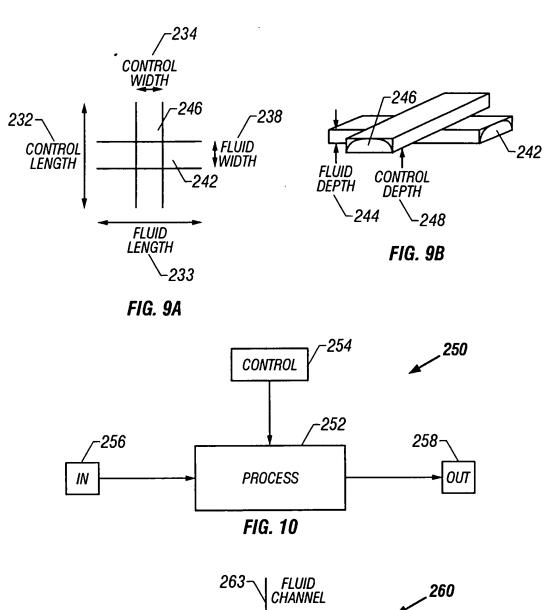
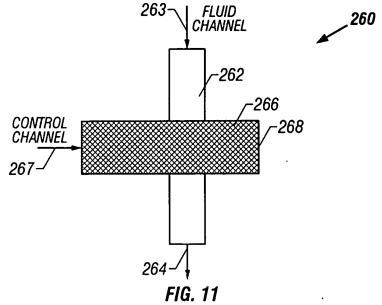


FIG. 7B

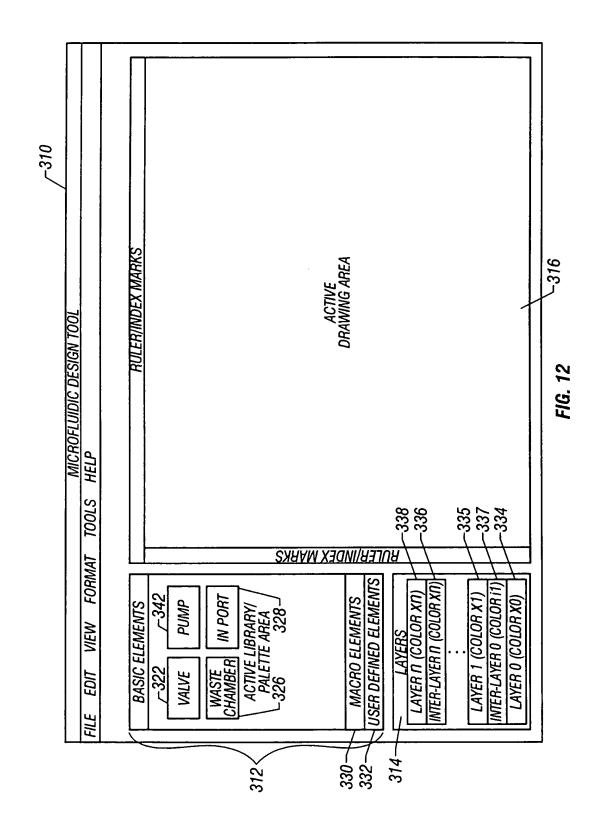


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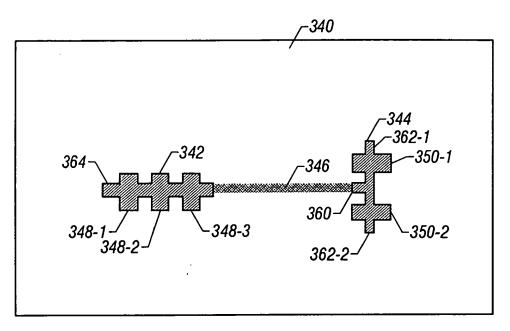


FIG. 13A

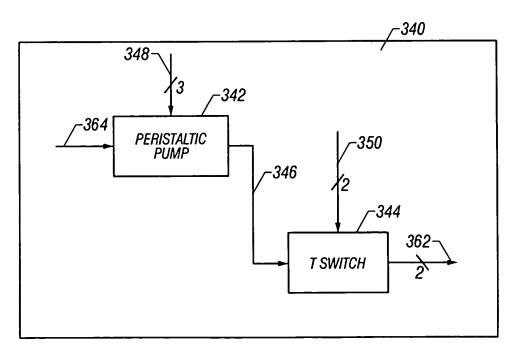


FIG. 13B

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380 — 370 — DIAGNOSTIC CHIP CONTROL LANGUAGE

320 — 370 — SCHEMATIC DESIGN DATABASE | INPUT PORTS | OUTPUT PORTS | PORTS | PORTS | RESULTS |

372 — 378 — 376 — 374

FIG. 14

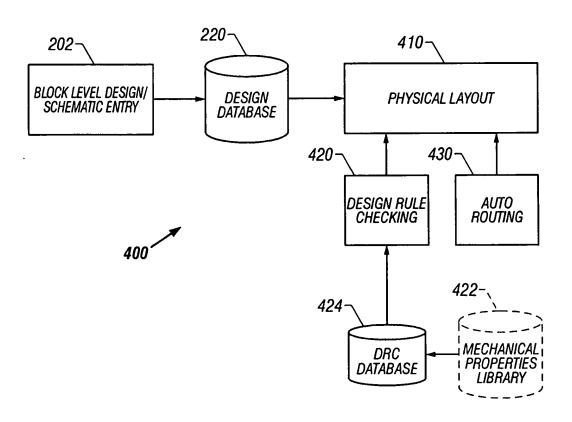


FIG. 15

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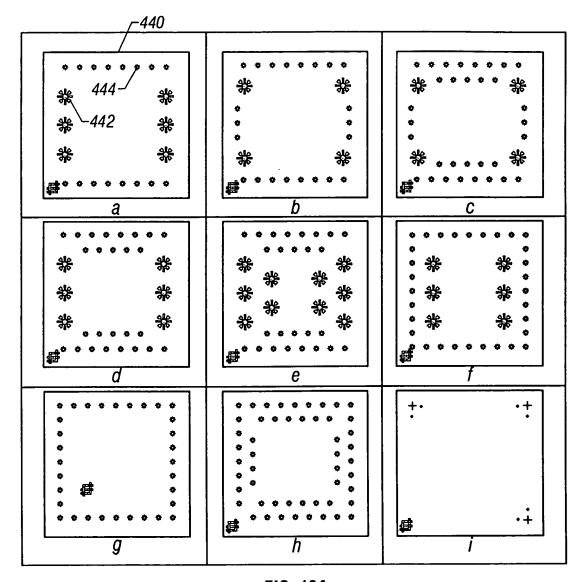
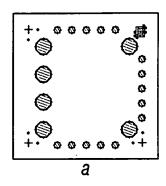
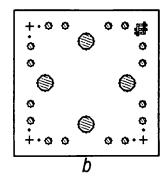
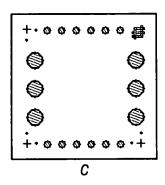


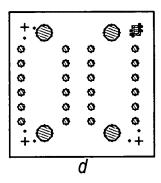
FIG. 16A

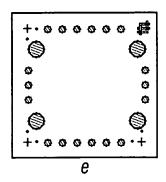
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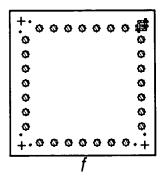


FIG. 16B

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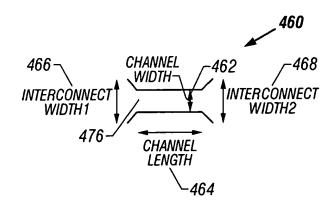
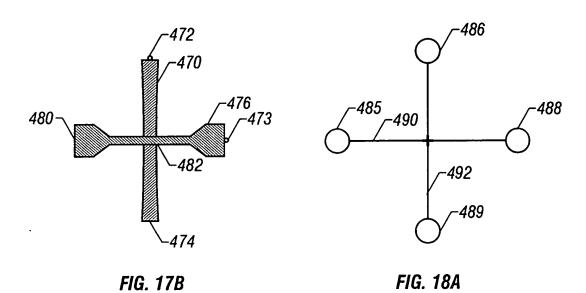


FIG. 17A



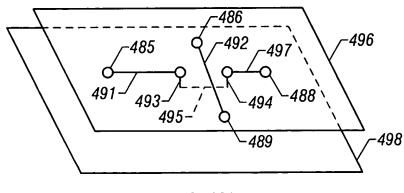
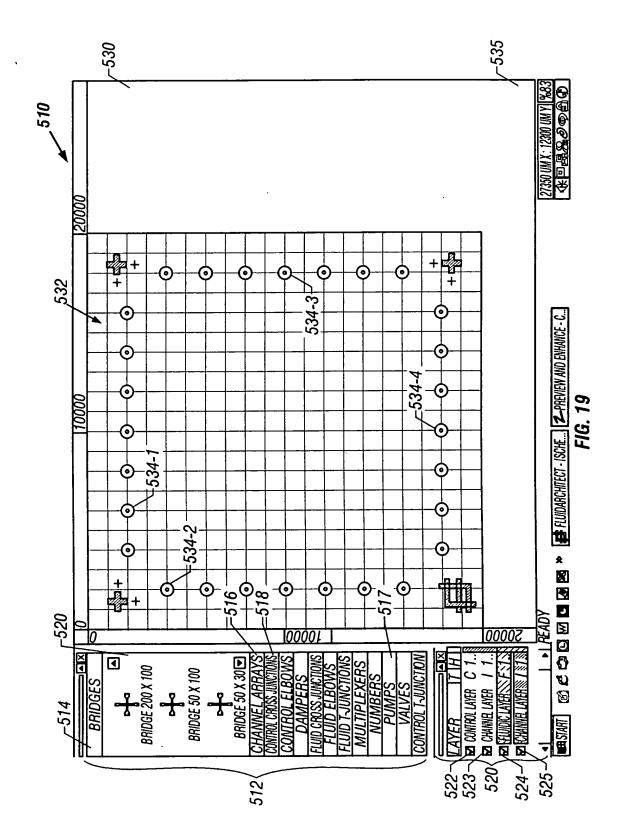


FIG. 18B

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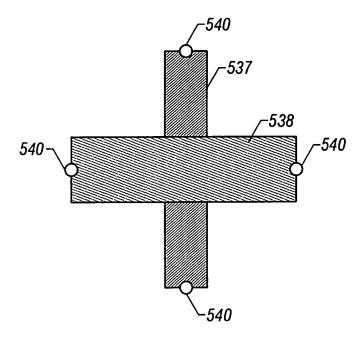
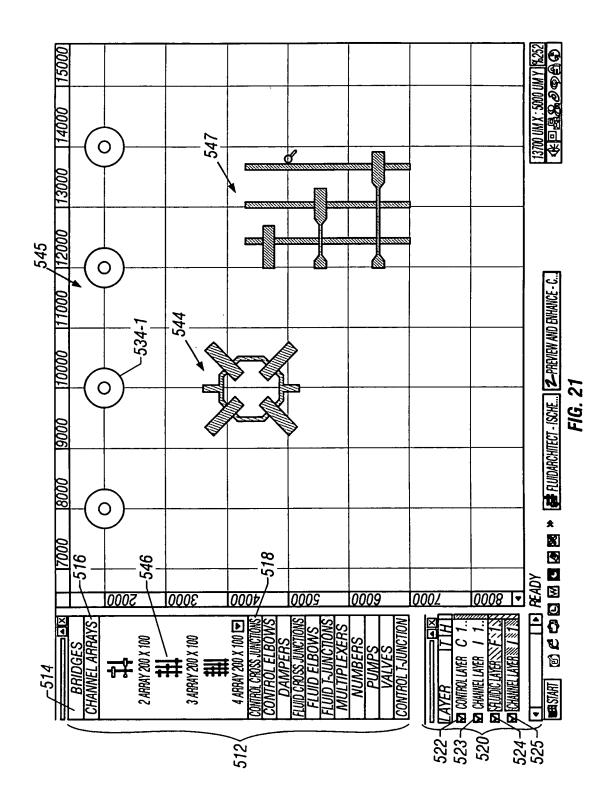
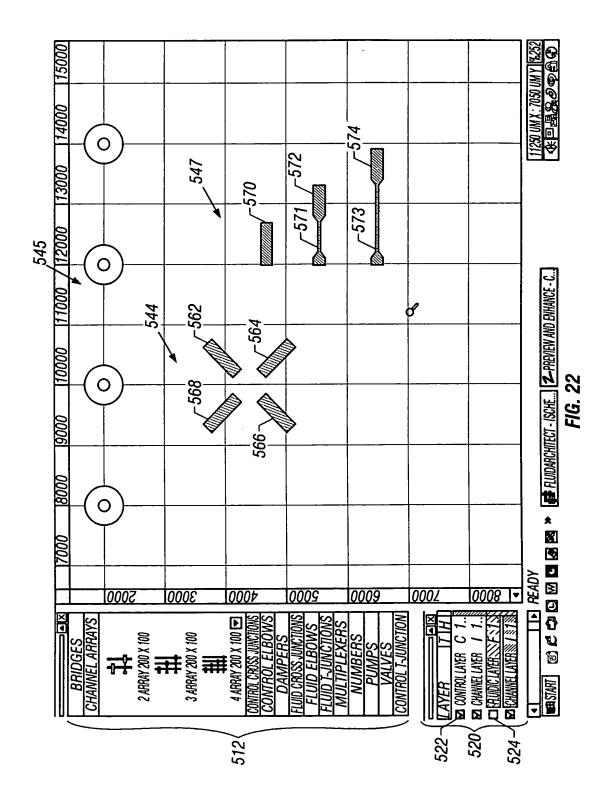


FIG. 20

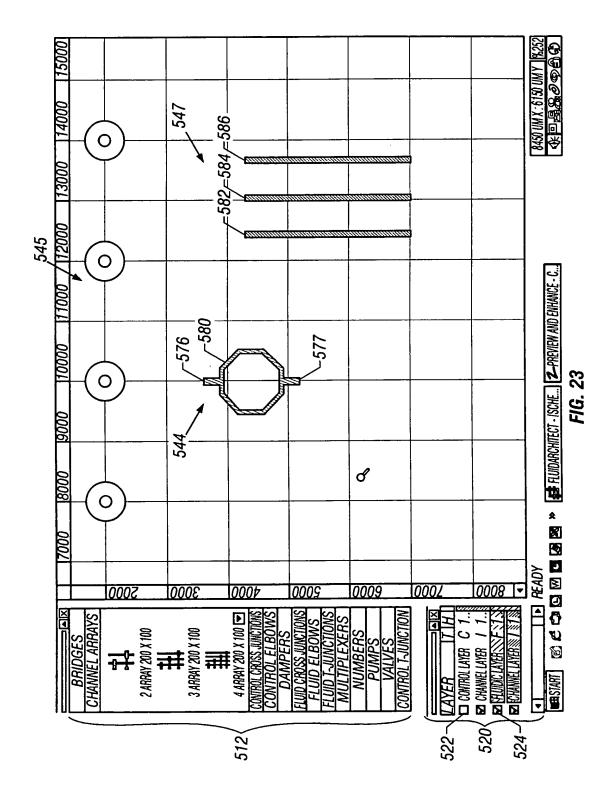
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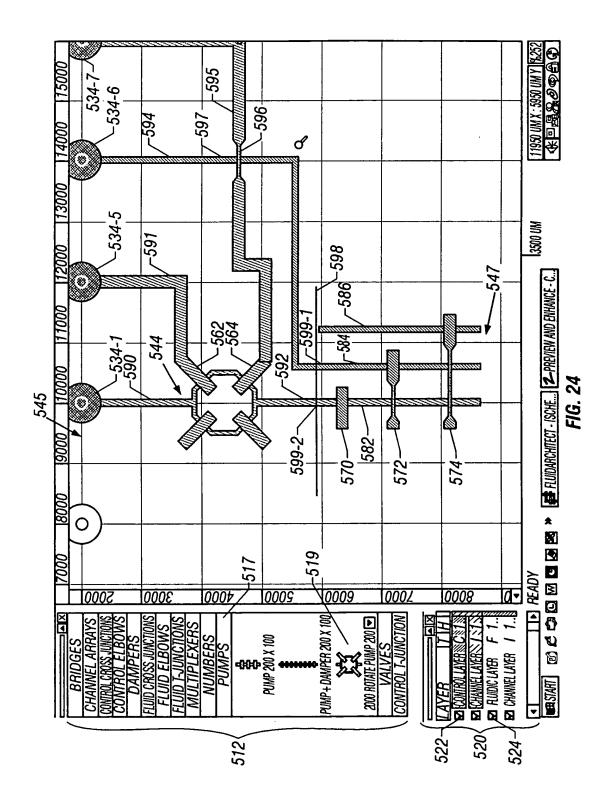
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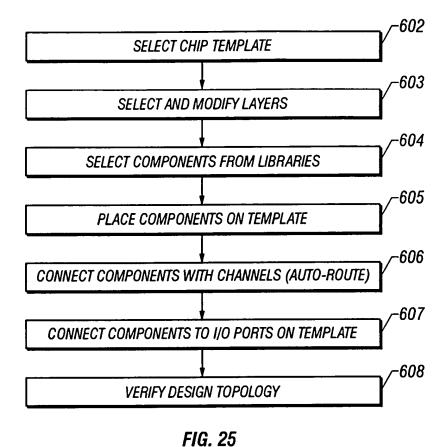


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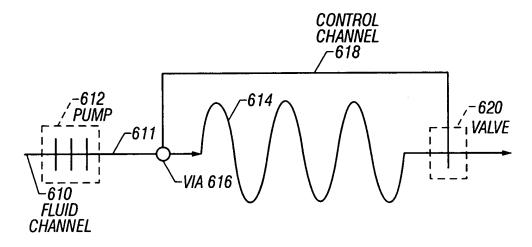
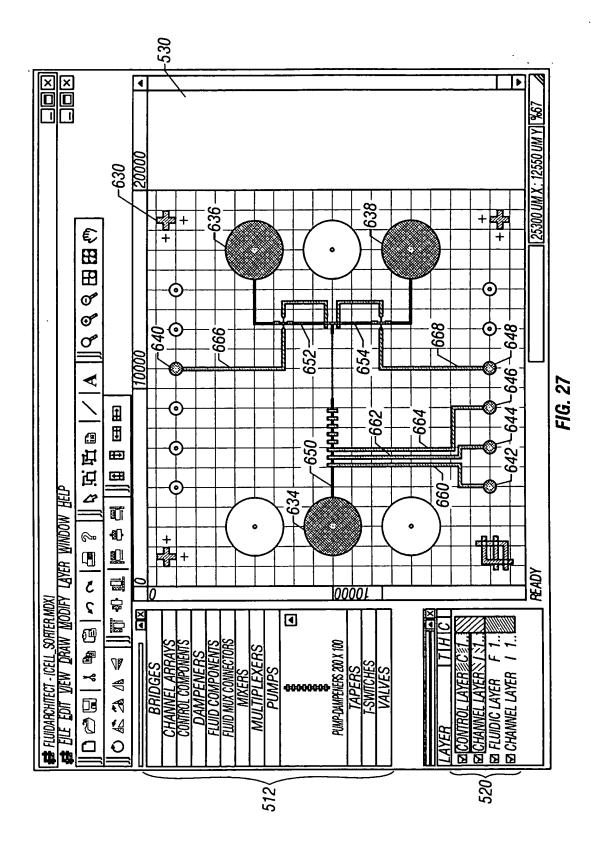


FIG. 26

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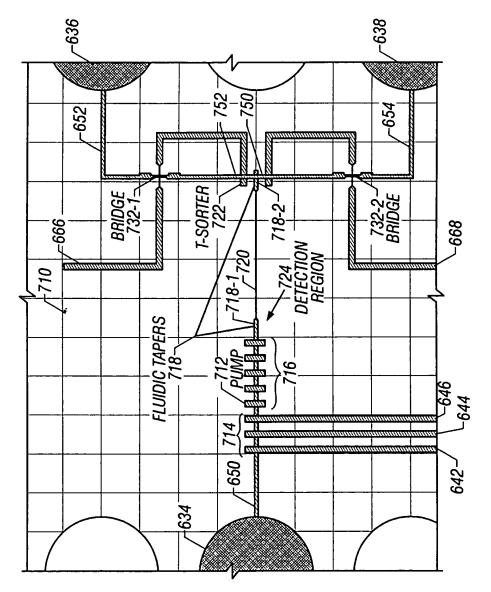


FIG. 28A

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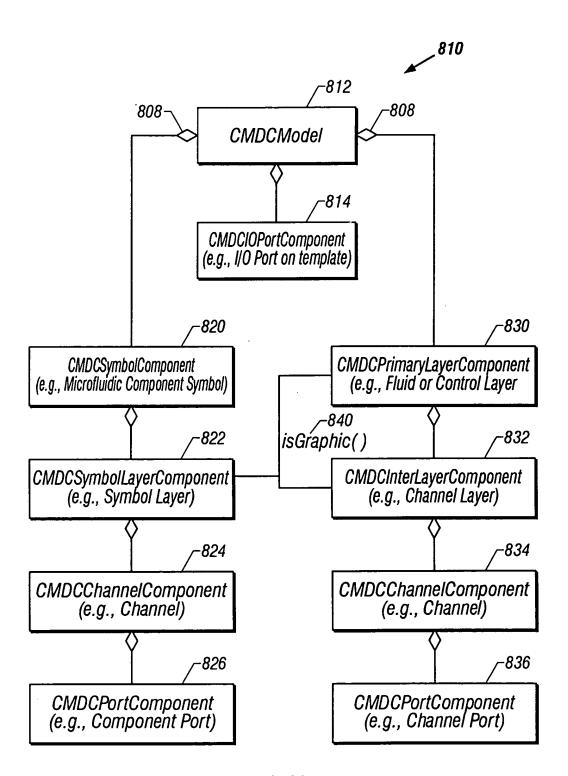


FIG. 28B

Titled: A Microfluidic Design Automation Method



